



AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

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Department of Electrical and Electronics Engineering

Lr.No. AIETM/EEE/BoS3

Date: 01.06.2026

MINUTES OF THE BOARD OF STUDIES MEETING HELD ON 01.06.2026 11.00 AM to 12.30 PM

Minutes of the meeting of Board of Studies, Department of Electrical and Electronics Engineering, held 11.00AM to 12.30 PM in our department.

Mode: Hybrid Mode






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Members Present:

S.No	NAME OF THE MEMBER & AFFILIATION	NOMINATION	SIGNATURE
1	Dr. K. Sri Kumar Professor, JNTU-GV CEV(A), VZM kotni.eee@jntugvcev.edu.in kotni.77@gmail.com Mobile:9440480242	Expert member nominated by University (JNTU-GV)	 11/6/26
2	Prof. Mallikarjuna Rao. Pasumarthi HOD and Professor, Dean TDR- HUB-AU Dept of Electrical Engineering College of engineering (A), Andhra university, Visakhapatnam, Andhra Pradesh e-Mail: head.eee@andhrauniversity.edu.in Mobile: 9848175818	Expert member nominated by Academic Council	
3	Dr. Ranjan Kumar Behera Associate Professor IIT Patna E-mail : rkb@iitp.ac.in Mobile: 9162227357	Expert member nominated by Academic Council	
4	Sri. M. Nageswara Rao DGM, NTPC Simhadri, Visakhapatnam Mobile no. 8331016103 Email: Nageswarm@gmail.com Makalanageswar@ntpc.co.in	Nominated from Industry	






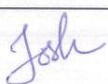

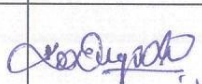
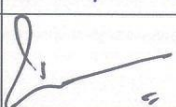

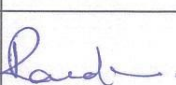
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5	Sri. K V S Rama Chandra Murthy, Professor, EEE Dept, & Dean (R&D) Aditya University, Surampalem, Kakinada District. e-Mail: murthy.kvs@aec.edu.in Mobile:7036266665	Expert from outside the Autonomous College	
6	Sri. Kuppilli. Sai Charan Technical Leader: PV & ESS- South & Central Asia, Jinko solar co Ltd, e-Mail: Saicharan.k@jinkosolar.com Mobile: 9999593334	Nominated from Alumini	
7	Dr C.P.V.N.J. MOHAN RAO	Principal	
8	VARAHALA DORA PETLA ASSISTANT PROFESSOR	Member	
9	Dr.D.Ramesh	Member	
10	MARRA JYOTHSNA SIVANI ASSISTANT PROFESSOR	Member	
11	KANCHARLA RAJANI ASSISTANT PROFESSOR	Member	
12	K DURGA RAO ASSISTANT PROFESSOR	Member	
13	GAJJI RAJASEKHAR YADAV ASSISTANT PROFESSOR	Member	
14	MUDE NAVYA KANTHA ASSISTANT PROFESSOR	Member	
15	CHILLA PALLI RAMADEVI ASSISTANT PROFESSOR	Member	








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16	SEELAM SUJATHA DEVI ASSISTANT PROFESSOR	Member	
17	P. SHEERISHA ASSISTANT PROFESSOR	Member	
18	K.NOOKA RAJU ASSISTANT PROFESSOR	Member	
19	L.ADVILA ASSISTANT PROFESSOR	Member	
20	Dr. T. SRINIVASA RAO Professor	Chairperson	

Minutes of the Meeting

Department of Electrical and Electronics Engineering (EEE)

1. Welcome Address:

Dr. T. Srinivasarao, Chairman, Board of Studies (EEE), welcomed all the members and expressed his gratitude for their presence and continued support in strengthening the curriculum and academic quality of the department. He highlighted the importance of revising the curriculum in line with emerging technologies, industry requirements, NEP 2020 guidelines, and AICTE recommendations.

2 Discussions, Review and Approval of the Proposed Curriculum for B.Tech. EEE (III & IV B.Tech. I and II Semesters) for AY 2026–2027

The proposed curriculum structure for III and IV B.Tech. EEE was presented before the Board. The curriculum was designed in accordance with JNTUGV regulations, AICTE Model Curriculum, and industry requirements.

The members reviewed the course structure, credit distribution, laboratory components, internship provisions, skill enhancement courses, professional electives, and open electives.

The following suggestions were made:

Dr. K. Sri Kumar, University Nominee, JNTU-GV, suggested that the Skill Enhancement Courses (Employability Skills Courses) should have uniform credit allocation from the First Year to Final Year to maintain consistency in curriculum structure and assessment. He emphasized that a standardized credit



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framework would facilitate better academic planning, equitable workload distribution, and uniform implementation across all semesters.

Prof. Mallikarjuna Rao Pasumarthi, Professor & Dean (R&D), Andhra University, appreciated the department for introducing emerging and industry-relevant courses such as Soft Computing Techniques and its Applications in Electrical Engineering, Drone Technology & Applications, and IoT Applications in Electrical Engineering. He observed that these technologies are highly relevant to current industry requirements and align well with Industry 4.0 and digital transformation initiatives.

Dr. K. Sri Kumar appreciated the department's efforts in aligning the curriculum with contemporary technological advancements and strongly recommended encouraging students to enroll in SWAYAM/NPTEL certification courses to enhance their knowledge and employability.

After detailed deliberations, the Board unanimously approved the proposed curriculum structure for III and IV B.Tech. EEE to be implemented from the Academic Year 2026–2027.

Agenda Item No. 3:

Discussion, Review and Approval of the Proposed Curriculum for M.Tech. (Power Electronics) Programmes for AY 2026–2027

The Chairman presented the proposed curriculum for **M.Tech. (Power Electronics)**.

The members appreciated the inclusion of advanced courses related to:

- Smart Grid Technologies
- Artificial Intelligence Techniques
- Renewable Energy Technologies
- Power Quality Improvement Techniques
- Energy Management and Auditing
- Research Methodology and IPR

The curriculum structure, electives, laboratories, audit courses, and dissertation components were thoroughly reviewed.

Resolution No. 3:

The Board unanimously approved the proposed curriculum and syllabus for M.Tech. (Power Electronics) programme for implementation from the Academic Year 2026–2027.

Agenda Item No. 4:

Finalization of Syllabus for III B.Tech. – I Semester (EEE)

The detailed syllabus of the following courses was presented and discussed:



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Core Courses

- Power Electronics
- Digital Circuits
- Power System Analysis

Professional Elective – I

- Utilization of Electrical Energy
- Signals and Systems
- Soft Computing Techniques and its Applications in Electrical Engineering
- Communication Systems
- SWAYAM/NPTEL Course Recommended by the BoS

Open Elective – I

- Concepts of Optimization Techniques
- Electrical Machine Design
- Intelligent Control Systems
- Renewable Energy Sources
- Concepts of Power System Engineering

Laboratories and Other Courses

- Power Electronics Laboratory
- Analog and Digital Circuits Laboratory
- Tinkering Laboratory
- Technical Paper Writing & IPR
- Employability Skills–III: Soft Skills and Verbal Ability
- Evaluation of Community Service Internship / Industry Internship

Dr. K. Sri Kumar, University Nominee, JNTU-GV, suggested including the textbook "Smart Grid Technologies" by Pramod Kumar Kamath and P. S. Bimbira as a recommended reference for the Smart Grid Technologies course.

Dr. K. Sri Kumar thoroughly reviewed the proposed syllabus and appreciated the efforts taken by the department in designing the curriculum. He emphasized that due care should be taken to avoid repetition of topics across different courses and semesters, ensuring effective utilization of course content and providing students with exposure to new and advanced concepts throughout the programme.

The syllabus for III B.Tech. I Semester courses was reviewed and unanimously approved by the Board for implementation from the Academic Year 2026–2027.



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Agenda Item No. 5:

Finalization of Syllabus for III B.Tech. – II Semester (EEE)

The syllabus of all core courses, professional electives, open electives, laboratories, Research Methodology course, Employability Skills–IV, and Mandatory Industry Internship was discussed in detail.

The Board appreciated the inclusion of:

- Machine Learning and Deep Learning Applications
- Industrial Automation and Control
- Full Stack Development
- Drone Technology and Applications
- IoT Applications in Electrical Engineering Laboratory

Dr. Ranjan Kumar Behera, Professor, IIT Patna, appreciated the revised curriculum and suggested encouraging students to undertake small projects and practical applications alongside theory courses. He stated that such activities would improve conceptual understanding, problem-solving abilities, and industry readiness.

The Board unanimously approved the syllabus for III B.Tech. II Semester for implementation from the Academic Year 2026–2027.

Agenda Item No. 6:

Finalization of Syllabus for IV B.Tech. – I Semester (EEE)

The syllabus of core courses, professional electives, open electives, AI Applications to Power Systems Simulation Laboratory, Industry Internship Evaluation, Indian Traditional Knowledge, and Employability Skills–V was reviewed.

Members appreciated the inclusion of:

- Renewable Energy Systems and Smart Grid
- Energy Management and Auditing
- Design of PV Systems
- Embedded System Design
- Artificial Intelligence Applications

Sri. M. Nageswara Rao, DGM, NTPC Simhadri, suggested including additional topics related to Power Systems Deregulation, electricity markets, power trading, and recent developments in power sector reforms to provide students with better exposure to the changing requirements of the power industry.



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Sri. Kupilli Sai Charan, Engineer, Jinko Solar, appreciated the inclusion of Renewable Energy Systems & Smart Grid and suggested strengthening the course by incorporating practical case studies on solar power plants, renewable energy integration with the grid, and smart grid applications.

The Board unanimously approved the syllabus for IV B.Tech. I Semester for implementation from the Academic Year 2026–2027.

Agenda Item No. 7:

Finalization of Syllabus for IV B.Tech. – II Semester (EEE)

The Board reviewed the Professional Elective–V basket and Open Elective–IV basket.

Special appreciation was expressed for introducing:

- Electric Vehicles and Battery Management Systems
- Switched Mode Power Conversion
- Electrical Power Quality
- Special Electrical Machines for Industrial Applications
- Quantum Computing
- Solar PV Systems

Sri. K. V. S. Rama Chandra Murthy, Dean (R&D), Aditya University, appreciated the well-designed curriculum and the efforts taken by the department in introducing emerging and industry-oriented courses. He particularly appreciated the inclusion of Power Electronics for Electric Vehicles (EVs), Soft Computing Techniques and its Applications in Electrical Engineering, and Power Systems Simulation with AI Applications.

The Internship and Project component was also discussed and approved. The syllabus for IV B.Tech. II Semester was unanimously approved by the Board.

Agenda Item Nos. 8, 9 and 10:

Finalization of Syllabus for M.Tech. (Power Electronics)

The syllabus of M.Tech. Power Electronics for I Year I Semester, I Year II Semester, and II Year I Semester was presented and discussed.



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M.TECH – POWER ELECTRONICS

SEMESTER – I

Professional Core (PC)

- Electrical Machine Modeling and Analysis
- Advanced Power Electronic Converters – I

Program Elective – I (PE-I)

- Modern Control Theory
- Power Quality Improvement Techniques
- Programmable Logic Controllers and Applications

Program Elective – II (PE-II)

- Artificial Intelligence Techniques
- Renewable Energy Technologies
- Reactive Power Compensation and Management

Laboratories

- Power Electronics Simulation Laboratory
- Power Converters Laboratory

Mandatory Course (MC)

- Research Methodology and Intellectual Property Rights (IPR)

Audit Course – I (AC)

- English for Research Paper Writing
- Disaster Management

SEMESTER – II

Professional Core (PC)

- Advanced Power Electronic Converters – II
- Power Electronic Control of Electrical Drives

Program Elective – III (PE-III)

- Power Electronics for Renewable Energy Systems
- Electric Vehicles and Design
- Digital Control Systems



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Program Elective – IV (PE-IV)

- Advanced Digital Signal Processing
- Applications of Power Converters
- Microcontroller Applications to Power Electronics

Laboratories

- Electric Drives Simulation Laboratory
- Electric Drives Laboratory

Project Work

- Mini Project with Seminar

Audit Course – II (AC)

- Constitution of India
- Value Education

SEMESTER – III

Program Elective – V (PE-V)

- Digital Signal Processing Controlled Drives
- Smart Grid Technologies
- Modeling and Simulation of Power Electronic Systems

Open Elective (OE)

- Industrial Safety
- Energy Audit, Conservation and Management
- Composite Materials

Dissertation Phase – I

- Research Work, Literature Survey, Methodology Development and Progress Evaluation

SEMESTER – IV

Dissertation Phase – II

- Thesis Work
- Research Contributions
- Final Dissertation Submission and Viva-Voce Examination

Dr. K. Sri Kumar, University Nominee, JNTU-GV, thoroughly reviewed the proposed M.Tech. (Power Electronics) curriculum and syllabi. He appreciated the department for adopting and following the JNTU-GV curriculum framework and emphasized maintaining alignment with the university syllabus to ensure academic quality, uniformity, and compliance with regulatory guidelines.



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The Board unanimously approved the syllabi for M.Tech. (Power Electronics) programmes for the Academic Year 2026–2027.

Agenda Item No. 11:

Operational Procedure for Dissertation Phase–II

The guidelines, evaluation methodology, review process, plagiarism policy, publication requirements, and final assessment procedure for Dissertation Phase–II were discussed. The Board approved the proposed operational procedure for Dissertation Phase–II.

Agenda Item No. 12:

Teaching-Learning Methodologies, Research Initiatives and Industry Interaction Activities

The members recommended:

- Project-Based Learning
- Experiential Learning
- Industry-Sponsored Projects
- Faculty Development Programmes
- Research Publications
- Industry Expert Lectures
- Internship Collaborations
- Innovation and Startup Activitie

The recommendations were approved and shall be implemented during the Academic Year 2026–2027.

Agenda Item Nos. 13 & 14:

Any Other Academic Matter and Miscellaneous Issues

The members appreciated the department's efforts in continuously updating the curriculum and suggested periodic review of emerging technologies and industry requirements.

All recommendations made by the Board were accepted for incorporation wherever applicable.

Vote of Thanks

Dr. C.P.V.N.J. Mohan Rao, Principal, and Dr. T. Srinivasarao, Chairman, Board of Studies (EEE), thanked all the members for their valuable suggestions and active participation. The meeting concluded with a vote of thanks to the Chair.



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Overall Resolution

The Board of Studies unanimously approved the curriculum structure and syllabi proposed for:

- III B.Tech. EEE (I & II Semesters)
- IV B.Tech. EEE (I & II Semesters)
- M.Tech. Power Electronics
- M.Tech. Power Systems

for implementation from the Academic Year 2026–2027.

Yours truly,

Chairperson

Board of Studies (EEE)

Copy to

1. All BoS Members
2. Dean Academics
3. Principal office

Chairperson
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Dept of Electrical & Electronics Engg,
Avanthi Inst of Engg & Tech. (Aut. 56)
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