(Approved by AICTE, Permanently Affiliated to JNT University Kakinada, Accredited by NAAC and Recognized under 2(f) &12 (b) by UGC, New Delhi) Tamaram, Makavarapalem, Narsipatnam (RD),

Visakhapatnam-531113



Certificate Course

วท

Zero to Chip Design using OpenPOWER

From 21st Feb 2022to 26th Feb 2022



ORGANIZED BY

DEPT. OF ELECTRONICS AND COMMUNICATION ENGINEERING AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY, MAKAVARAPALEM (V), VISAKHAPATNAM-531113

AVANTHI EDUCATIONAL SOCIETY

Avanthi Educational Society under the Leadership of Sri M.Srinivasa Rao garu as chairman was started in the Year 1991. Within a short span of its establishment, the group has made a remarkable stride in the field of education offering various courses at Under Graduate, Post Graduate, Pharmacy & Engineering levels. This milestone is achieved as the institution carved itself to impart quality and career oriented education, countering the challenges of the modern world through planning, dedication, determination, prompt execution and with the innovative ideas of our advisory board.

Today, Avanthi Educational Society is proud to have a strength of over 16000 students with 15 institutions under its ambit. It is the path of glory towards the success during the last 19 years. The institution has been adjudged many times as the second best educational institutions in the twin cities and 16th best in all over India through the impartial survey made by the renowned magazine "India Today".

AVANTHI INSTITUTE OF ENGINEERING & TECHNOLOGY

AIET started in the year 1999 and offers various courses at Engineering and PG level. The college is providing with rooms, computer centre, laboratories and seminar hall with audio-visual equipments. Industry Institute interaction is conducted regularly to emphasize on the latest trends in the present market.



It is very near to Narsipatnam. Frequent bus facilities are available both from and to Visakhapatnam and Narsipatnam. Very safe and secure hostel facility is available for Girl students. These are the additional facilities besides excellent academic atmosphere in the college campus.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

The ECE Department was established in the year 1999 with an intake of 60 students and this was increased to 120 students in the 2007 and increased to 180 students in the 2012. The department has an eminent faculty and well supported infrastructure and laboratories. The faculty keeps abreast with the latest advances in technology and ensures that hardware equipment and related software are upgraded to ensure its students are able to keep pace with current trends in technology and the industry.

The department also offers a Post – Graduate courses in DECS and VLSI Design. The students are encouraged to participate in workshops, industrial internship, industrial visits and seminars; along with the projects assigned during the course, these activities enable them to broaden their outlook and build in professionalism that makes their transition from college to industry smoother after graduation

ABOUT COURSE

The main objective of the program is to provide basic concepts of SoC design, introduce OpenPOWER cores, VLSI testing and verification concepts. The course covers the concepts and tools you need throughout the entire VLSI pipeline

TOPICS TO BE COVERED

Day-1:introduction to OpenPOWER

Day-2: Microwatt Introduction

Day-3:GEM5 for processor evaluation

Day-4: Verification concepts - Introduction

Day-5: System on Chip (SoC) and its Components

Day-6: Chip Alliance and Automated Analog design flow

For Registration please contact Mrs G.Sandhya, Assistant Professor, ECE

CHIEF PATRON

Smt.M.Gnaneswari President, Avanthi Educational Society

PATRON

Dr. C P V N J Mohan Rao Principal, Avanthi Institute Of Engineering And Technology

CHAIRMAN

Dr. E. Govinda Head of the Department Electronics and Communication Engineering

Tamaram, Makavarapalem, Narsipatnam (RD), Visakhapatnam-531113

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CIRCULAR

Date: 17/02/2022.

This is informed to all the III& II B.Tech ECE students that our department is planning to conduct one week course on "Zero to Chip Design using OpenPOWER" scheduled from 21-02-2022 to 26-02-2022. It is directed to all the students of III and II ECE to utilize this opportunity to enhance your technical skills. For more details about registration process and participation contact Course Coordinator Mrs.G.Sandhya, A: sistant Professor, ECE.

Resource Person Details:

- 1. Dr .PA. Nageswara Rao Associate Professor. Department of ECE, Gayatri vidya parishad college, Rushikonda, Visakhapatnam.
- 2. Dr.J.Sudhakar. Professor, Department of ECE, Vignan's Institute of Engineering for Women, Visakhapatnam.

Head of the Department,

Copy to: Principal, AIET Avanthi Institute of Engg. & Technology Tamaram, Makavarapalem Md., Visakhapatnam District., Pin: 531113

ECE. HEAD OF THE DEPARTMENT DEPARTMENT OF ECE Avanthi Institute of Engg.&Tech. Makavarapalem, Visakhapatnam Dist-53- 113.



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Tamaram, Makavarapalem, Narsipatnam (RD), Visakhapatnam-531113

Zero to Chip Design using OpenPOWER

From 21st Feb 2022 To 26th Feb 2022

SYLLABUS

Day-1:introduction toOpenPOWER

Day-2: Microwatt Introduction

Day-3:GEM5 for processor evaluation

Day-4: Verification concepts - Introduction

Day-5: System on Chip (SoC) and its Components

Day-6: Chip Alliance and Automated Analog design flow

E.Govinda
CHAIRMAN
CHAIRMAN
Asst. Professor

We
Head of the Department
Electronics and communication Engi eering

DEPARTMENT OF ECE

Avanthi Inetitute of Engg. & Tech.

Makatarapalem, Visathapatnam Dist. 53: 113.



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Zero to Chip Design using OpenPOWER

From 21st Feb 2022 To 26th Feb 2022

<u>Schedule</u>

Day-1 (21/02/2022)

09:00 to 10:00	Inaugural ! ession
10:00 to 12:00	introduction toOpenPOWER
12:00 to 01:00	Lunch Bre k
01:00 to 04:00	IC design v orkloads running on Clouds

Day-2 (22/02/2022)

09:00 to 12:00	Microwatt Introduction
12:00 to 01:00	Lunch Bre. k
01:00 to 04:00	FPGA Implementation of Microwatt system demo

Day-3(23/02/2022)

09:00 to 12:00	GEM5 for processor evaluation
12:00 to 01:00	Lunch Bre. k
01:00 to 04:00	FPGA Implementation of Microwatt system Hands on

Day-4(24/02/2022)

09:00 to 12:00	Verification concepts - Introduction	
12:00 to 01:00	Lunch Bre k	
01:00 to 04:00	Components of IP Core verification	

Day-5(25/02/2022)

09:00 to 12:00	System on Chip (SoC) and its Components
12:00 to 01:00	Lunch Break
01:00 to 04:00	Fabless SoC Ananth and SoC verification methodologies

Day-6(26/02/2022)

09:00 to 12:00	Chip Alliance and Automated Analog design flow
12:00 to 01:00	Lunch Break
01:00 to 04:00	Automated RTL to GDS II Flow and conclusion

E. Govinda CHAIRMAN

&

Head of the Department Electronics and Communication Engineering G. Sandhya
Asst. Professor
Coordinator

HEAD OF THE DEPARTMENT DEPARTMENT OF ECE

Avanthi Institute of Engg.&Tenti. Meksyarapolem, Visakhapetnem Dist-50



Tamaram, Makavarapalem, Narsipatnam (RD), Visakhapatnam-531113

Zero to Chip Design using OpenPOWER

From 21st Feb 2022 To 26th Feb 2022

III& II BTECH ECE STUDENT ATTENDANCE LIST

S.NO	ROLL NO	NAME IF THE STUDENT	21/2	22/2	23/2	24/2	25/2	26/2
1.	19811A0401	ADATRAVU BHARGAVI	V	V	1	1	1	V
2.	19811A0402	ALLU ROOPA	V	V		V	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	1
3.	19811A0404	BALIJI YAMINI	/	/				~
4.	19811A0405	BEVARA DIVYASREE	/	V	1	V	~	1.
5.	19811A0406	BHAVANA BUDDA		/	/	X		
6.	19811A0407	BODAPATI SAMU SRI		V		\ \ \		
7.	19811A0408	BYLAPUDI VINITH	/		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	/		
8.	19811A0409	KANTIPAMU SASIKALA		/	/			
9.	19811A0410	CHITIKALA PRADEEP CHANDRA			/	/		
10.		CVVTD LD						
1.1	19811A0411	CHITRADA RAMESH	\	/			/	~
11.	19811A0412	CHOCHUPATLA KUSUMALATHA		/	1	/	/	~
12.	19811A0413	CHUA BALAJI	V	1	/	5	1	/
13.	19811A0415	DEENAMKONDA GOPAL RAJU	/	/	5	5	1	/
14.	19811A0416	DEEPTHI PEDAPATI	/	X	/	/	V	✓
15.						V		
	19811A0417	GANISETTY DIVAYA JYOTHI		✓	/			
16.	19811A0418	GANUGULA BHARAT KUMAR		/	✓	/	/	/
17.	19811A0419	GAVIREDDI DEVI		/	/		/	/
18.	19811A0420	GEDDAM TEJASWI		/		V	/	✓
19.								
	19811A0421	KARAKA PYDIRAJU	/	\checkmark				X
20.	. 19811A0422	KEERTHI ROHINI	/	/	/	V	~	/

19811A0423	KEMBURU SAISREE	~	V	V	~	~	-
19811A0424	KILAPARTHI SAI KANNAYYA NAIDU	/	/	-	1	~	V
19811A0425	KODA MANIKANTA	/	~	~	V	V	V
19811A0426	KOTLA KRISHNA KUMAR	~		/	/	V	V
19811A0427	KAMBALA LIKHITHA	/	/	~	V	V	V
19811A0428	MEDE NAGA SRAVANI	~	~	~	V	~	V
19811A0429	MENDI DENIL	/	X	~	~	V	/
19811A0430	MUNASALA HEMA LATHA					~	
19811A0431	MUPPANADAM VENKATA MANIKANTA SAI	/					
19811A0432	NADIGATLA MAHALAKSHMI	/					
19811A0433	NAKKA GANESH	1	/	/	/	/	/
19811A0434	NAKKA MANASA	V	/	/	/	1	/
19811A0435	PADI DEVI	/	V		/	V	/
19811A0436	PALLADI DIVYA		/		V	V	/
. 19811A0437	PAMPANA MANI JYOTHSNA					V	
19811A0438	PILLI MOHAN GOWRI KALAVATHI	V	/	V		~	/
19811A0440	POTHUNEEDI PAVAN KALYAN				/	X	/
19811A0441	RAPETI HEERAVANI		~	~		/	/
	SALADULA POORNA CHANDRASEKHAR	/	/	/	/	V	/
		/	V	V	/	V	/
		V	/	/	/	/	/
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43.	19811A0447	TALARI MOHAN VASANTH KUMAR	V	/	~	-	V	/
44.	19811A0448	TATIKONDA NANI	~	~	~		-	V
45.	19811A0449	TEPPALA TARUN		~	V	/	V	V
46.	19811A0450	TONANGI PAVANI		/	/	~		~
47.	19811A0451	TURUBILLI KOTESWARA RAO	~	~	/	/	~	~
48.	19811A0452	VUDI KIRAN	~	/		/		
49.	19811A0453	YERNAGULA RAHUL		/	V		~	~
50.	20815A0401	ALAPATI ANASUYA PRABHAVATHI TEJASWI	V	~	V	/	~	V
51.	20815A0402	ANDIBOINA DHARANI	/	V	V	/	~	V
52.	20815A0403	ANIVILLA PADMINI PRASHANTHI		/		/	/	V
53.	20811A0401	ADAPUREDDI HEMA KIRAN	V				1.	V
54.	20811A0403	ANNAMREDDY RENUKA		X			/	-
55.	20811A0404	A R JUSTINA	~					
56.	20811A0405	AZAD TEPPALA	/	/				
57.	20811A0406	BANDARU GANGADHAR			/	/		V
58.	20811A0407	BANDARU HARIKA		V				
59.	20811A0408	BANDARU UDAYKAMAL		./	. /			1
60.	20811A0409	BANTUBILLI SIRISHA	/		V	1	/	1
61.	20811A0410	BARNIKALA LOKESH			/		<u></u>	
62.	20811A0411	BEERA MAHESH BABU			./		/	/
63.	20811A0412	BHEEMUNI YASASWANI NAMRATHA						1
64.	20811A0413	SRI BODDETI PRASANTHI	V	\/	V	/	V	V
65.	20811A0414	B. NIKHIL TEJA		1	/		V	1
66.	20811A0415	BOTTA TULASI	/		/	1		
67.	20811A0416	CHANDAKA PAVANI	/	V	/		V	V
68.	20811A0417	CHIKKALA.GOWRI PARVATHI DEVI	/	_	/	/	1	
69.	20811A0418	ERLA GURU PRATHAP	/		/	/	/	/
70.	20811A0419	G SRI HARSHA	. /	/	1	V		V
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73. 20811A0422 GOKULAPATI SHYAM 74. 20811A0423 G DEVAMANIKANTIA 75. 20811A0424 GONTHINA DEEPTHI 76. 20811A0425 GUNISETTY TANMAYEE SATYA 77. 20811A0425 GUNISETTY TANMAYEE SATYA 78. 20811A0427 J SRINIVAS SARAT 78. 20811A0428 KETHAVARAPU. BHARGAVI 79. 20811A0429 KILLAMPALLI PRASANNA 80. 20811A0430 KONA MOHAN KARTHIK 81. 20811A0431 KORATANA TEJA 82. 20811A0433 KORRAYI DHARAMALLESH 83. 20811A0434 JAYALAKSHMI NANDAVARAPU 84. 20811A0435 LEKKALA KALYANI 85. 20811A0436 MADEM MANITEJA 86. 20811A0436 MADEM MANITEJA 87. 20811A0438 MARRAPU SURESH 88. 20811A0440 MIDATHANA SRIVANI 90. 20811A0441 MOLLETI DURGAPRASAD 91. 20811A0445 SUDHAKAR NAGIREDDY 94. 20811A0445 SUDHAKAR NAGIREDDY 94. 20811A0446 NAMALA TEJASWINI 95. 20811A0447 PADMANABHAM.SAI BHARGAVI 96. 20811A0448 PALLELA LAVANYA 97. 20811A0445 PENTAKOTA JASHWANTH KUMAR 99. 20811A0445 PENTAKOTA JASHWANTH KUMAR 90. 20811A0455 POLUMURI APPALARAJU 101. 20811A0455 POLUMURI APPALARAJU 104. 20811A0455 POLUMURI APPALARAJU	72.	20811A0421			T				
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	311A0476	YAKA POORNA VENKATESH	•/			./		
124. 2081	311A0477	YEDLA PUSHPA LATHA			1	1		
	311A0478	YENNI GNANESWARA RAO					~	~
125. 2081	311A0479	YERUVA BALESWAR REDDY	1		/	/	/	/
126. 2081	311A0481	KODUKULA ASWININ GANESH LIKITH KUMAR		V	1			V
127. 2181	315A0401	KANDREGULA GEETHA SRI LAKSHMI	1	./				
128. 2181	315A0402	KUSIREDDI TULASIRAM				./		
129. 2181	315A0403	LALAM KUSUMA NAGA GANA MALLIKA		~		V	/	/
130. 2181	315A0404	LANKA LAKSHMI SUBHA SRI HAVYA	V	/		/	/	/
131. 2181	315A0405	PARAVADA BHANU PRASAD	/	/	/		V	
132. 2181	315A0406	POLAMARASETTI JAGADEESH	/	/				
133. 2181	315A0407	PONNADA PRIYANKA	/	/		/	./	1
134. 2181	15A0408	POOLLA VENKATA SRIVANI BHARGAVI	/	V		1	/	/
135. 2181	15A0409	VANGURI POORNA SEKHAR	/	V	./	/	V	
136. 2181	15A0410	YENUGULA MANEESHA	/	V			-	2
137. 2181	15A0411	CHEPURI HEMALATHA	/	V			V	-
138. 2181	315A0413	GARAGA SAMUEL BABU	1/	V			1/	
139. 2181	15A0414	KISTAM MOUNIKA		/	1	/	-	

140.	21815A0415	KONATHALA SASI KANTH		/	/	/	~	V
141.	21815A0416	PENTAKOTA JAYANTH	/	/	/	/	/	~
142.	21815A0417	SAMMETLA VARSHINI	/	/	/	V	/	/
143.	21815A0418	BODDAPATI YESWANTH	1	/	/	/	~	V
144.	21815A0419	SETTI UMA VENKATA VARALAKSHMI	1	V	~	/	V	

Gandhya

HEAD OF THE DEPARTMENT

DEPARTMENT OF ECE
Avanthi Institute of Engg.&Tech.

Makavarapalem, Visakhapatnam Dist-53- 113.



(Approved by AICTE, Permanently Affiliated to JNT University Kakinada, ACCREDITED BY NAAC and Recognized under 2(f) &12 (b) by UGC, New Delhi) Tamaram, Makavarapalem, Narsipatnam (RD), Visakhapatnam-531113

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Course on

Zero to Chip Design using OpenPOWER

Dated :- 21/02/2022



G. Sardhya Coordinator

Head of the Department

HEAD OF THE DEPARTMENT
DEPARTMENT OF EGE
Avanthi Institute of Engg. & Tech.
Minararuparem. Visakhapatnam Dist-53- 113.



(Approved by AICTE, Permanently Affiliated to JNT University Kakinada, ACCREDITED BY NAAC and Recognized under 2(f) &12 (b) by UGC, New Delhi)

Tamaram, Makavarapalem, Narsipatnam (RD), Visakhapatnam-531113

Department of Electronics and Communication Engineering

Dt:01/03/2022

BRIEF REPORT

Avanthi Institute of Engineering and Technology had conducted a certification course on "Zero to Chip Design using OpenPOWER" From 21st Feb 2022 to 26th Feb 2022 in the Department of Electronics and Communication Engineering.

We had Dr.PA.N: geswara Rao, Associate Professor, Depar ment of ECE, Gayatri vidya parisł ad college, Rushikonda, Visakhapatnam ir troduced open power and microwatt ope ation and evaluated GEM 5 processor.

Dr.J. Sudhakar, Pi of essor, Department of ECE, Vignan's Institute of Engineering for Women, Visakhapatnam explained chip verification, soc chip alliance and concluded with the design flow of chip.

G. Sandhya Coordinator

Head of the Department

HEAD OF THE DEPARTMENT
DEPARTMENT OF EGE
Avanthi Institute of Engg. & Tech.
Mekavarapaiem. Visakhapatnam Dist-53* 113.



AVANTHI

INSTITUTE OF ENGINEERING AND TECHNOLOGY

(Approved by AICTE, Permanently Affiliated to JNT University Kakinada, accredited by NAAC and Recognized under 2(f) &12 (b) by UGC, New Delhi)

Tamaram, Makavarapalem, Narsipatnam (RD), Visakhapatnam-53111

Certificate of Participation

This is to certify that Mr./Ms	of _	has
Participated in the Certificate Course entitled on Zero t	to Chip Design using Open POWI	ER conducted from
21-02-2022 to 26-02-2022 was organized by the Depart	tment of ELECTRONICS AND COMMUN	NICATION
ENGINEERING at Avanthi Institute of Engineering and Te	echnology.	

Coordinator HOD Principal