



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA**  
**KAKINADA – 533 003, Andhra Pradesh, India**

**DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING**

**COURSE STRUCTURE & SYLLABUS for  
M.Tech EEE Common for**

- I. Power Electronics (PE)**
- II. Power and Industrial Drives (P&ID)
- III. Power Electronics and Electrical Drives (PE &ED)
- IV. Power Electronics and Drives (PE&D)
- V. Power Electronics and systems (PE&S)
- VI. Electrical Machines and Drives (EM&D)

**Programme**

*(Applicable for batches admitted from 2019-2020)*



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**COURSE STRUCTURE**

**I Semester**

S.No	Course No	Category	Course Name	P.Os	L	T	P	C	Marks
1		PC	Electrical Machine Modeling and Analysis		3	0	0	3	100
2		PC	Analysis of Power Electronic Converters		3	0	0	3	100
3		PE	Elective – I i. Modern Control Theory ii. Power Quality and Custom Power Devices iii. Programmable Logic Controllers & Applications		3	0	0	3	100
4		PE	Elective – II i. Artificial Intelligence Techniques ii. Renewable Energy Technologies iii. HVDC Transmission and Flexible AC Transmission Systems		3	0	0	3	100
5			Research Methodology and IPR		2	0	0	2	100
6			Power Electronics Simulation Laboratory		0	0	4	2	100
7			Power Converters Laboratory		0	0	4	2	100
8			Audit Course – 1		2	0	0	0	100
					16	0	8	18	800

**II Semester**

S.No	Course No	Category	Course Name	P.Os	L	T	P	C	Marks
1		PC	Switched Mode Power Conversion		3	0	0	3	100
2		PC	Power Electronic Control of Electrical Drives		3	0	0	3	100
3		PE	Elective – III i. Control & Integration of Renewable Energy Systems ii. Hybrid Electric Vehicles iii. Digital Control Systems		3	0	0	3	100
4		PE	Elective – IV i. Advanced Digital Signal Processing ii. Applications of Power Converters iii. Microcontrollers		3	0	0	3	100
5			Electric Drives Simulation Laboratory		0	0	4	2	100
6			Electric Drives Laboratory		0	0	4	2	100
7			Mini Project with Seminar		0	0	4	2	100
8			Audit Course – 2		2	0	0	0	100
					14	0	12	18	800



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**III Semester**

S.No	Course No	Category	Course Name	P.Os	L	T	P	C	Marks
1		PE	Program Elective – V i. Digital Signal Processing Controlled Drives ii. Smart Grid Technologies iii. Modeling & Simulation of Power Electronic Systems		3	0	0	3	100
2		OE	Open Elective i. Industrial Safety ii. Energy Audit, Conservation & Management iii. Composite Materials		3	0	0	3	100
3			Dissertation Phase - I (to be continued and evaluated next semester)		0	0	20	10	---
					6	0	20	16	200

**IV Semester**

S.No	Course No	Category	Course Name	T	P	C	Marks
1			Dissertation Phase-II (continued from III semester)	0	32	16	100
				0	32	16	100

**Audit course 1 & 2**

1. English for Research Paper Writing
2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by Yoga
8. Personality Development through Life Enlightenment Skills.



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<b>I-Semester</b>	<b>POWER ELECTRONICS SIMULATION LABORATORY</b>	<b>CATEGORY</b>	<b>L-T-P 0 -0-4</b>	<b>CREDITS 2</b>
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**Course Educational Objectives:**

To analyze the operation of DC-DC converters, AC-DC converters and DC-AC converters by simulation.

**Any 10 of the following experiments are to be conducted.**

**List of Experiments:**

1. Simulation of Buck converter using small signal model.
2. Simulation of Boost converter using small signal model.
3. Simulation of single phase half bridge inverter.
4. Simulation of single-phase full bridge inverter using Uni-polar & Bi-polar PWM techniques.
5. Simulation of three phase inverter using sine-triangle PWM.
6. Simulation of three phase inverter using space vector PWM.
7. Simulation of three level three phase NPC inverter.
8. Study of neutral point voltage floating in NPC three level inverter
9. Simulation of 3-level flying capacitor inverter & evaluation of capacitor voltage balanced methods.
10. Simulation of single phase AC voltage regulator.
11. Simulation of three phase AC voltage regulator.
12. Comparison of harmonic profile of two level & three level inverter (FFT analysis).
13. Simulation of 5-level inverter using carrier based PWM methods.
14. Simulation of three phase full converter with RL & RLE loads.
15. Simulation of three-phase dual converter.

**Course Outcome:** To understand the operation of DC-DC converters, AC-DC converters, AC voltage regulators and DC-AC converters by simulation.



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I-Semester	POWER CONVERTERS LABORATORY	CATEGORY	L-T-P	CREDITS
			0-0-4	2

**Course Educational Objectives:**

To study and understand the different converters and inverters for single and three phase loads.

**Any 10 of the following experiments are to be conducted.**

**List of experiments**

1. Study of DC-DC non-isolated converters such as Buck & Boost converter.
2. Study of DC-DC Buck-Boost and Cuk converters.
3. Study of 1- $\phi$  dual converter.
4. Determination of input p.f. and harmonic factor for 1- $\phi$  semi-converter and 1- $\phi$  full-converter (Inductive load)
5. Study of p.f. improvement in 1- $\phi$  full-converter with symmetric and extinction angle control.
6. Study of 1- $\phi$  square wave and sinusoidal PWM inverter.
7. Study of 3- $\phi$  inverter with 120° and 180° mode of operation.
8. Study of 3- $\phi$  sinusoidal PWM inverter.
9. Study of 3-level NPC inverter.
10. Study of 5-level cascaded H-bridge inverter.
11. Determination of input p.f. and harmonic factor for 3- $\phi$  full converter (Inductive load).
12. Determination of input p.f. and harmonic factor for 3- $\phi$  semi converter (Inductive load).
13. Study the characteristics of IGBT, MOSFET & GTO's.
14. Design of gate drive circuits for IGBT & MOSFET's.

**Course Outcomes:** Students are able to implement the converter and inverters in real time applications.



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<b>II-Semester</b>	<b>Electric Drives Simulation Laboratory</b>	<b>Category</b>	<b>L-T-P 0-0-4</b>	<b>Credits 2</b>
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**Pre-requisite:** Power electronics & Drives

**Course Educational Objectives:**

The student should be able to understand the simulate different electrical machines and drives

**Any 10 of the following experiments are to be conducted.**

**List of Experiments:**

1. Simulation of DC shunt machine as motor & generator.
2. Simulate the speed control of DC motor using chopper converter.
3. Simulation of induction motor modes using d-q model.
4. Simulate the speed control of induction motor by using V/f control.
5. Simulate the BLDC motor and observe the speed transients.
6. Simulate speed control of induction motor by using vector control.
7. Compare the transient performance of induction motor controlled by v/f control & vector control methods.
8. Simulate PMSM motor by using d-q model.
9. Simulate the multi-level inverter fed induction motor drive.
10. Simulate the re-generative braking of inverter fed induction motor.
11. Study of PWM controlled inverter fed PMSM drive.
12. Evaluation of switching frequency effect on electric drive

**Course Objectives:**

The student should analyze the performance of different electrical machines and drives



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<b>II-Semester</b>	<b>Electric Drives Laboratory</b>	<b>Category</b>	<b>L-T-P</b> <b>0-0-4</b>	<b>Credits</b> <b>2</b>
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**Course Educational Objectives:**

To study the speed control methods of DC & AC drives.

**Any 10 of the following experiments are to be conducted.**

**List of experiments:**

1. Study of armature controlled separately excited DC drive with 1- $\phi$  full converter.
2. Study of chopper controlled separately excited DC drive.
3. Study of armature controlled separately excited DC drive with 3- $\phi$  full converter
4. Study of dynamic braking of DC drives.
5. Study of regenerative braking of DC drive.
6. Study of performance characteristics of a 3- $\phi$  induction motor using V/f control.
7. Vector control based speed control of induction motor.
8. Study of direct torque control of induction motor.
9. Speed control of PMSM drive with 3- $\phi$  inverter.
10. Speed control of BLDC drive with 3- $\phi$  inverter.
11. Speed control of switched reluctance motor drive.

**Course Outcome:**The student should Understand the performance of DC & AC drives.



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<b>II-Semester</b>	<b>Mini Project with Seminar</b>	<b>Category</b>	<b>L-T-P</b> <b>0-0-4</b>	<b>Credits</b> <b>2</b>
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**Note:**

It is recommended that a Supervisor/advisor should be allotted to each student at the end of the semester-I or allot at the start of the semester-II

**Syllabus content:**

A Student has to select one paper published in any of the IEEE Transactions and simulate the same. The student has to present the progress of the work at the middle of the semester. At the end of the semester, the student has to present the results by explaining the idea of the topic, methodology, finding of the simulations. A Student should also submit a report of the entire work carried out under this course. The end semester presentation must be video recorded and preserved.