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Certificate Course

on

LAB VIEW

From 24TH SEP 2018 TO 28TH SEP 2018



ORGANIZED BY

DEPT. OF ELECTRICAL &ELECTRONICS ENGINEERING AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY, TAMARAM (V), MAKAVARAPALEM (M), VISAKHAPATNAM-531113

AVANTHI EDUCATIONAL SOCIETY

Avanthi Educational Society under the Leadership of Sri M.Srinivasa Rao garu as chairman was started in the Year 1991. Within a short span of its establishment, the group has made a remarkable stride in the field of education offering various courses at Under Graduate, Post Graduate, Pharmacy & Engineering levels. This milestone is achieved as the institution carved itself to impart quality and career oriented education, countering the challenges of the modern world through planning, dedication, determination, prompt execution and with the innovative ideas of our advisory board. Today, Avanthi Educational Society is proud to have a strength of over 16000 students with 15 institutions under its ambit. It is the path of glory towards the success during the last 19 years. The institution has been adjudged many times as the second best educational institutions in the twin cities and 16th best in all over India through the impartial survey made by the renowned magazine "India Today".

AVANTHI INSTITUTE OF ENGINEERING & TECHNOLOGY

AIET started in the year 1999 and offers various courses at Engineering and PG level. The college is providing with rooms, computer centre, laboratories and seminar hall with audio-visual equipments. Industry Institute interaction is conducted regularly to emphasize on the latest trends in the present market.



It is very near to Narsipatnam. Frequent bus facilities are available both from Visakhapatnam and Narsipatnam. Very safe and secure hostel facility is available for

Girl students. These are the additional facilities besides excellent academic atmosphere in the college campus.

DEPARTMENT OF ELECTRICAL ANDELECTRONICS ENGINEERING

The Department of Electrical & Electronics Engineering was established in the year 2001 along with the MECHANICAL, ECE and CSE departments. The department has an intake of 60 students. Commencing the academic year 2011, a post-graduate course in Power Electronics and 2014 in Power Systems was started. The department has full fledged laboratories, workshops and P.G. labs. The department is staffed with qualified, experienced and dedicated members who engage in research activities.. The department with active support from the management is aiming to expand its research activities and industrial consultancies.

ABOUT COURSE

The LabVIEW Course helps you explore the LabVIEW environment, dataflow programming, and common LabVIEW development techniques.In this course, you will learn how to develop data acquisition (DAQ), instrument control,

Principal, Avanthi Institute Of Engineering And Technology

Dr. C P V N J Mohan Rao

PATRON

Dr. T Srinivasa Rao Head of the Department Electrical & Electronics Engineering

CHAIRMAN

tronics Engineering G Rajasekhar Yadav

G Rajasekhar Yadav Asst. Professor

COORDINATORS

P Anil Kumar

Asst. Professor

data-logging, and measurement analysis applications. At the end of the course, you will be able to use various editing and debugging techniques and create applications using the state machine design pattern to acquire, process, display, and store real-world data with DAQ devices.

TOPICS TO BE COVERED

Day 1:Navigating LabVIEW and creating a simple Labview application

Day 2: Troubleshooting and debugging techniques in LabVIEW

Day 3: Creating and leveraging structures

Day 4: Modular programming in LabVIEW

Day 5: Labview design techniques and the state machine design pattern

For any further information Contact Mr P ANIL KUMAR, Assistant Professor, EEE, and Mr G RAJASEKHAR YADAV Assistant Professor, EEE

CHIEF PATRON

Smt.M.Gnaneswari President, Avanthi Educational Society



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Dt. 18th Sep, 2018

CIRCULAR

A Short term Course on *LabVIEW* for all the II Year I Semester students of Electrical & Electronics Engineering Department is scheduled from 24th Sep 2018 to 28th Sep 2018. All the students should attend the course without fail. For any further information Contact Mr. P ANIL KUMAR, Mr G Rajasekhar Yadav, Assistant Professor, EEE Department.

Resource Person Details:

1.Sri N. Surya Narayana Multi Skill Trainer APSSDC

 Kum G Sireesha
 Multi Skill Trainer APSSDC

T. Srinivasa Rac

Head of the Department
Head of the Department
Department of Electrical & Electronics Engg.
Avanthi Institute of Engg & Tech.
Makavarapalem, Visakhapatnam - 531113.

Copy to: Principal, AIET



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LABVIEW

Duration:24-09-2018 to 28-09-2018

SYLLABUS

Day 1: Navigating LabVIEW and creating a simple Labview application

Day 2: Troubleshooting and debugging techniques in LabVIEW

Day 3: Creating and leveraging structures

Day 4: Modular programming in LabVIEW

Day 5: Labview design techniques and the state machine design pattern

P. Anil Kunnar COORDINATOR

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Department of Electrical & Electronics Engg.
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Mekavarapalem, Visakhapetnam - 531113.



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Tamaram, Makavarapalem, Narsipatnam (RD), Visakhapatnam-531113

LABVIEW

SCHEDULE

Duration:24-09-2018 to 28-09-2018

DAY/DATE	Live Session - 1 09.00AM to 12.30 PM	Live Session - 2 1 PM to 4 PM						
Monday 24/09/2018	Navigating LabVIEW and creating a simple Labview application	Creating a simple Labview application						
Tuesday 25/09/2018	Troubleshooting and debugging techniques in LabVIEW	Excute labview code and techniques for managing loop execution						
Wednesday 26/09/2018	Creating and leveraging structures	Decession making labview structures						
Thursday 27/09/2018	Modular programming in LabVIEW	Acquiring measurements with hardware						
Friday 28/09/2018	Accessing files in labview	Labview design techniques and the state machine design pattern						

P. Anil Kuman COORDINATOR

HØD
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LABVIEW From 24th Sep 2018 TO 28th Sep 2018 2nd Year Students Attendance

SNO	ROLL NUMBER	NAME OF THE STUDENT	24-Sep	25-Sep	26-Sep	27-Sep	28-Sep
1	17811A0201	AMARAPALLI NAGA SAI	V	V	V	$\sqrt{}$	V
2	17811A0202	BABBOTHI RAMYA		$\sqrt{}$	$\sqrt{}$	$\sqrt{}$	V_
3	17811A0203	BAVIRISETTY PESALAYA	$\sqrt{}$	$\sqrt{}$	$\sqrt{}$	$\sqrt{}$	V_
4	17811A0204	CHINTAKAYALA BINDU MADHURI		$\sqrt{}$		$\sqrt{}$	\checkmark
5	17811A0205	GOLLU VARALAKSHMI		$\sqrt{}$	$\sqrt{}$		V
6	17811A0206	KURACHA SAI SOWJANYA				\checkmark	\checkmark
7	17811A0207	LALAM SWAMY	V	X	\checkmark		\checkmark
8	17811A0208	LALAM VENKATA NOOKAMBIKA		$\sqrt{}$	$\sqrt{}$	V	\checkmark
9	17811A0209	MATHIREDDY SHIVAKUMAR		\checkmark		$\sqrt{}$	
10	17811A0210	MEDISETTI DURGAPRASAD		$\sqrt{}$	$\sqrt{}$	\checkmark	V
11	17811A0211	PYLA BHASKARRAO			$\overline{}$		\checkmark
12	17811A0212	RAJANA RAMACHANDRA RAO		-	V	\checkmark	
13	17811A0213	RAVADA PAVAN GANESH		V	V	\checkmark	\checkmark
14	17811A0214	RAVADA PRAKASH RAO		\checkmark	X	/	\checkmark
15	17811A0215	YEDDU PREETHI		$\sqrt{}$	$\sqrt{}$	\checkmark	V
16	18815A0201	ADAPA SRINIVAS		\checkmark	$\sqrt{}$	\checkmark	$\sqrt{}$
17	18815A0202	ADDURI PRADEEP KUMAR			$\overline{}$	\checkmark	$\sqrt{}$
18	18815A0203	AKULA AMARTHYA		\checkmark	\checkmark	\checkmark	\checkmark
19	18815A0204	AMIREDDI SREEDHAR		$\sqrt{}$	$\sqrt{}$		
20	18815A0205	BADAPU BHUVANESWARI			/	/	
21	18815A0206	BARNIKALA CHIRANJEEVI		\checkmark	V	\checkmark	/
22	18815A0207	BEESETTI PAVAN SAI	\times	$\sqrt{}$	\checkmark	\checkmark	\checkmark
23	18815A0208	CHAPPA RAMA KRISHNA PRASAD		\checkmark	\checkmark	\checkmark	\checkmark
24	18815A0209	CHIRIKI KRANTHIMOHAN		V	V	\checkmark	
25	18815A0210	DARLA THARAKA NAGA SAI KUMAR	/	\checkmark	/	$\sqrt{}$	$\sqrt{}$
26	18815A0211	DARLA VENKATESH	V	V	Y	\checkmark	/
27	18815A0212	DHARA KALYAN		V	V	$\sqrt{}$	V
28	18815A0213	GARAGA SALDURGA	V	V	√	V	\checkmark
29	18815A0214	GATTA KOTI RAJESH	V.	V	V .	X	
30	18815A0215	GORLE POLAMAMBA DEVI	V	$\sqrt{}$	V	\checkmark	
31	18815A0216	GULIMI VASU	IV.	V	\checkmark	/	V
32	18815A0217	INDHALA SANDEEP	V	V	V	V	/
33	18815A0218	KANDULA CHANDRAMOULI	V	V .	V	V	/
34	18815A0219	KONDRA DIVYASREE	V ,	V	1/	V	Y
35	18815A0220	LAKKIDAPU ADVILA	V	V,	V,	ν,	V
36	18815A0221	MALLA VENKATESH	V	V,	V	V	V
37	18815A0222	NEMALA NAGALAKSHMI	IV.	V,	V	V,	V
38	18815A0223	PANDURI VENKATA SAI KUMAR	V	V	V,	V	/
39	18815A0224	PEDAPATI DIVYA PRACHOTHAN	IV.	V,	V	V	/
40	18815A0225	PENDYALA SIVA DURGA ANIL KUMAR	V	V,	V,	V	X
41	18815A0226	PRODDUTURI RAJA SEKHAR	V	V	$\sqrt{}$	V	V
42	18815A0227	R SARAT SATEESH	1	V	V.	/	V.
43	18815A0228	SALAPU NAGA SATISH		V	V	V	V
44	18815A0229	SARIPALLI APPALARAJU	V	V	V	V	V
45	18815A0230	SARIPALLI KESAVARAO	X	L V	V	V	V

46	18815A0231	SUREDDY HAREESH				$\sqrt{}$	/
47	18815A0232	TIRUVEEDULA KRISHNA SAI	\/	/	V	/	/
48	18815A0233	VURUKUTI DILEEP KUMAR		/	//	/_	1
49	18815A0234	MYLARA LOKESH		X	/	V	V
50	18815A0235	BATHI RAMESH		/	/_	/_	1
51	18815A0236	JAVVADI VENKATA NAVEEN KUMAR		1	1	V	1
52	18815A0237	NEELAPU NAGA SRINIVASA REDDY	V	\checkmark	/	V	V

P. Anil Kiman

HOD

Head of the Department
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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

a course on

LAB VIEW

From 24-09-2018 to 28-09-2018





P. Anil Kiman Coordinator

HOD EEE Department

Department of Electrical & Electronics Engg. Avanthi Institute of Engg & Tech. Makavarapalem, Visaknapatnam - 531113.



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Certificate of Participation

This	is	to	certify	that	Mr.,	/Ms.									of
				_has	part	icip	ated	in	the	Cert	ifica	te	Course	enti	tled
on LA	B V	IEW	_during	from	24 TH	SEP	2018	_to	28 TH	SEP	2018	was	orga	nised	by
the D	epa:	rtm	ent of E	ELECTR	ICAL	AND	ELEC	CTRC	NICS	ENG	INEER	ING	at At	anthi	
Insti	tute	e o	f Engine	eering	and	Tec	hnolo	ogy.							

Coordinator

HOD

Principal



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Department of Electrical and Electronics Engineering

Dt:01-10-2018

BRIEF REPORT

Department of Electrical and Electronics Engineering, Avanthi Institute of Engineering and Technology had organized a certificate course on "LAB VIEW" during 24-09-2018 to 28-09-2018

The speaker Sri N. Surya Narayana (Multi Skill Trainer APSSDC) started the session from the very basic with hands-on and he explained the use of each and every command with practical examples. The students participated actively in the event by interacting with full of energy. In the first half of first day, the basics of labview along with trouble shooting and debugging in labviewwas covered. The speaker also taught them the way to develop modular programming in labview. All these were practically done by the students in the Lab. The students have gained enough knowledge from the practical examples and practical sessions.

Kum G. Sireesha (Multi Skill Trainer APSSDC) explained creating and leveraging structures to the students in detail. The two sessions dealt with practical real time example where the resource person used myrio to generate and acquire signals. The students could generate Counter signals using myrio in FPGA under his guidance. The entire process of designing a simple Temperature control application was done in the Lab by the students under his guidance. The main objective of the programme was to learn labview design techniques and the state machine design pattern. From the course students have gained knowledge about how to approach Engineering problems using labview.

Coordinator

Head of the Department
Head of the Department
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