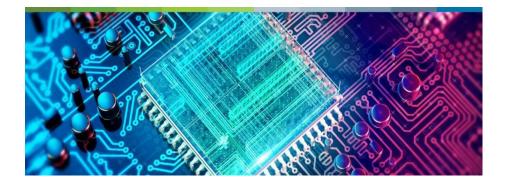
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Certificate Course on Embedded System Design

From 26th July 2018 to 01 August 2018



ORGANIZED BY DEPT. OF ELECTRONICS AND COMMUNICATION ENGINEERING AVANTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY, MAKAVARAPALEM (V), VISAKHAPATNAM-531113

AVANTHI EDUCATIONAL SOCIETY

Avanthi Educational Society under the Leadership of Sri M.Srinivasa Rao garu as chairman was started in the Year 1991. Within a short span of its establishment, the group has made a remarkable stride in the field of education offering various courses at Under Graduate, Post Graduate, Pharmacy & Engineering levels. This milestone is achieved as the institution carved itself to impart quality and career oriented education, countering the challenges of the modern world through planning, dedication, determination, prompt execution and with the innovative ideas of our advisory board.

Today, Avanthi Educational Society is proud to have a strength of over 16000 students with 15 institutions under its ambit. It is the path of glory towards the success during the last 19 years. The institution has been adjudged many times as the second best educational institutions in the twin cities and 16th best in all over India through the impartial survey made by the renowned magazine "India Today".

AVANTHI INSTITUTE OF ENGINEERING & TECHNOLOGY

AIET started in the year 1999 and offers various courses at Engineering and PG level. The college is providing with rooms, computer centre, laboratories and seminar hall with audio-visual equipments. Industry Institute interaction is conducted regularly to emphasize on the latest trends in the present market.



It is very near to Narsipatnam. Frequent bus facilities are available both from and to Visakhapatnam and Narsipatnam. Very safe and secure hostel facility is available for Girl students. These are the additional facilities besides excellent academic atmosphere in the college campus.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

The ECE Department was established in the year 1999 with an intake of 60 students and this was increased to 120 students in the 2007 and increased to 180 students in the 2012. The department has an eminent faculty and well supported infrastructure and laboratories. The faculty keeps abreast with the latest advances in technology and ensures that hardware equipment and related software are upgraded to ensure its students are able to keep pace with current trends in technology and the industry.

The department also offers a Post – Graduate courses in DECS and VLSI Design. The students are encouraged to participate in workshops, industrial internship, industrial visits and seminars; along with the projects assigned during the course, these activities enable them to broaden their outlook and build in professionalism that makes their transition from college to industry smoother after graduation.

ABOUT COURSE

Fiber optic systems area unit vital telecommunication infrastructure for world-wide broadband networks. Wide information measure signal transmission with low delay could be a key demand in gift day applications. Optical fibers give huge and unexceeded transmission information measure with negligible latency, and area unit currently the transmission medium of alternative for long distance and high rate transmission in telecommunication networks. This paper offers an outline of fiber optic communication systems together with their key technologies, and conjointly discusses their technological trend towards future generation..

TOPICS TO BE COVERED

- **Day-1:** Introduction to Embedded system
- **Day-2:** Characteristics of an embedded system
- **Day-3:** Embedded Firmware Design
- > **Day-4:** Hardware Software Co Design
- **Day-5:** Interrupt sources
- > **Day-6:** Application-specific domain

For Registration please contact Mr.R. Aneel Kumar, Assistant Professor, ECE

CHIEF PATRON	PATRON	CHAIRMAN
Smt.M.Gnaneswari	Dr. C P V N J Mohan Rao	Sri. E. Govinda
President, Avanthi Educational Society	Principal,	Head of the Department
	Avanthi Institute Of Engineering And Technology	Electronics and Communication Engineering



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Department of electronics and communication Engineering

Dt: 02/08/2018.

BRIEF REPORT

Avanthi Institute of Engineering and Technology had organized a certificate courseon "Embedded System Design" From 26th July 2018 to01 August 2018 in the Department of electronics and communication Engineering.

We had Miss D. CHANDI RANI, APSSDC gave the introduction on Embedded Systems, 8051 family, Arduino and Arduino Programming. And also she demonstrated few projects using Arduino board covering LED Based Projects, Interfacing of switches with Arduino, LCD Based Projects and Motor Based Projects.

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Head of the Department

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HEAD OF THE DEPARTMENT DEPARTMENT OF ECE Avanthi Institute of Engg.&Tech. Nokavarapalem, Visakhapatham Do-51 111



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

<u>CIRCULAR</u>

Date: 23/07/2018.

This is informed to all the III B.Tech ECE students that our department is planning to conduct one week course on "*Embedded System Design*" scheduled from 26-07-2018 to 01-08-2018. It is directed to all the students of III ECE to utilize this opportunity to enhance your technical skills. For more details about rejistration process and participation contact Course Coordinator Mr R.Aneel Kumar, Assistant Professor, ECE.

Resource Person Details:

E.CHANDI RANI,

Andhra Pradesh State Skill Development Corporation

E. Govinda

Head of the Department,

ECE.

HEAD OF THE DEPARTMENT DEPARTMENT OF ECE Avanthi Institute of Engg.&Tech, Makavarapalem, Visakhapatnam Dist-53: 113

Copy to: Principal, AIET



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Embedded System Design

From 26-07-2018 to 01-08-2018

SYLLABUS

- Day-1:Introduction to Embedded system
- Day-2:Characteristics of an embedded system
- Day-3:Embedded Firmware Design
- Day-4:Hardware Software Co Design
- Day-5:Interrupt sources
- > Day-6:Application-specific domain

E.Govinda CHAIRMAN & Head of the Department of Electro: ics and communication Engineering

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R. Aneel Kum r Asst. Professc r

Coordinate r

HEAD OF THE DEPARTMENT DEPARTMENT OF ECE Avanthi Institute of Engg.&Toch. Makavarapalem, Visakhapatnam Disto



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Embedded System Design

From 26th July 2018 to 1st August 2018

<u>Schedule</u>

Day-1 (26/07/2018)

troduction to Embedded system
unch Break
tegrated circuits, programmable logic devices
u

Day-2 (27/07/2018)

09:00 to 12:00	Characteristi s of an embedded system
12:00 to 01:00	Lunch Break
01:00 to 04:00	Quality attributes of embedded

Day-3 (28/07/2018)

09:00 to 12:00	Embedded Firmware Design Embeded Firmware design approaches,
12:00 to 01:00	Lunch Break
01:00 to 04:00	Embedded Firmware development languages, ISR concept,

Day-4 (30/07/2018)

09:00 to 12:00	Hardware Software Co Design Fundamental Issues In Hardware
12:00 to 01:00	Lunch Break
01:00 to 04:00	Software Co Design

Dav-5 (31/07/2018)

09:00 to 12:00	Interrupt sources, Interrupt servicing mechanism Multiple Interrupts system.
12:00 to 01:00	Lunch Break
01:00 to 04:00	Explanation of mos switches

Day-6 (01/08/2018)

Application-specific and Domain- Specific examples of an embedded system.
Lunch Break
Terminating simulation and conclusion

-

E. Govinda CHAIRMAN & Head of the Department Electronics and Communication Engineering

R. Anel Kumon

R. Aneel Kumar Asst. Professor

Coordinator

HEAD OF THE DEPARTMENT DEPARTMENT OF ECE Avanthi Institute of Engg.&Tech Makavarapalem, Visakhapatnam Dist-533 113.



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Embedded System Design From 26th JULY 2018 To 1st AUGUST 2018

III B.Tech ECE STUDENTS ATTENDANCE LIST

S.NO	Regd No	Name of the Student	26/7	27/7	28/7	30/7	31/7	1/8
1	16811A0401	ADDEPALLI NAVYA SRI	~	\checkmark	\checkmark	\checkmark	/	/
2	16811A0402	ALLA LAKSHMI	\checkmark	\checkmark	\checkmark	\checkmark	/	/
3	16811A0403	BAILAPUDI TARUN KUMAR	\checkmark	\checkmark		\checkmark	/	/
4	16811A0404	BALIREDDI MOHANKUMAR	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	V
5	16811A0405	BANTUPALLI BHASKAR RAO	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	V
6	16811A0406	BATHI SAI PRASANNA	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	/
7	16811A0407	BATTALA DILEEP KUMAR	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark
8	16811A0408	BATTULA USHA	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	ν
9	16811A0409	BIKKAVILLI SAMBASIVA RAO	\checkmark	\checkmark	\checkmark	\checkmark	V	V
10	16811A0410	BOBBILI VAMSI	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark
11	16811A0411	BODDEDA SAIRAM	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark
12	16811A0412	BOLEM VENKATA RAVI TEJA	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark
13	16811A0413	BOMMALI LEELAMOHAN	\bigvee	\sim	\sim	\checkmark	\checkmark	V
14	16811A0414	BURRI MANASA	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	\sim
15	16811A0415	CHADARAM MOUNIKA NOOKA ADI KUMARI	\checkmark	\checkmark	\checkmark	X	\checkmark	\checkmark
16	16811A0416	CHINNI HEMANAGESWARA RAO	V	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark
17	16811A0417	DATLA LAXMI SIVA SAHITHI		\checkmark	\checkmark	\checkmark	\checkmark	
18	16811A0418	DIMILI RENUKA	\checkmark	\checkmark		\checkmark	\checkmark	
19	16811A0419	DODDI GIRIDHAR SAI	V		\checkmark			V
20	16811A0420	GANGARAPU MANIKANTA RAJA VARA PRASAD	\checkmark	\checkmark	\checkmark	\checkmark	~	V
21	16811A0421	GANGUPAM MOUNIKA	\checkmark	V		~		
22	16811A0422	GARAGA YUGA SRI	\checkmark	V		./		
23	16811A0423	HITHESHI SANJAY HAZARNIS						
24	16811A0424	KAKI LALITHA	$\overline{\mathbf{X}}$					
25	16811A0425	KOLLI JAYASRI						- v
26	16811A0426	KONCHA ARJUNAMMA	$\overline{\mathbf{V}}$		V /			
27	16811A0427	KOSANA ASWINI	$\overline{\checkmark}$					
28	16811A0428	KOTA CHITTIBABU	V			-		
29	16811A0429	KOYILADA V V PARASURAM						
30	16811A0430	KUNDRAPU SUSEELA						
31		KUPPILI CHANDAN MEHER						
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51	16811A0451		V		~		V	V
52	16811A0452	PUSARLA SAI KEERTHANA	N/		~	~	~	~
53	16811A0453	RACHAPATTI HAREESH	V	- V	~	~	V	V
54	16811A0454	RAPETI BHARATH SAI			0		V	~
55	16811A0455	RAPETI SRINIVAS	V /	V	V		V	
56	16811A0457	SAPPA KOMALI		~	\sim	~		V
57	16811A0458	SARAGADAM MANEENDRA	V	V	\checkmark			V
58	16811A0459	SUNDARAPU SURYAKALA	,V	V	\checkmark	V		V
59	16811A0460	SURLA ROHINI	V	-V_	\checkmark			
60	16811A0461	THAMADA RAASI	V	V	\checkmark			V
61	16811A0462	TOGARU KRANTHI	V	\checkmark	\sim	V		
62	16811A0462	UDDANDAM MADHAV KUMAR		\checkmark	\checkmark	\sim		
63				\checkmark	V	\checkmark	V	V
	16811A0464	VARADA KUMAR	V	\checkmark	\sim		V	\sim
64	16811A0465	VASIREDDY VISHNU VARDHAN	V	\checkmark	\checkmark	\checkmark	\bigvee	V
65	16811A0466	VIJAY PRASAD	V	\checkmark	\checkmark		V	\vee
66	16811A0467	VIVEK PATNAIK	V	V	\checkmark	\vee	\checkmark	\checkmark
67	16811A0468	VOMMI SAEBHA RANI	V	\checkmark	V	\checkmark	\bigvee	
68	16811A0469	VYAKARANAM LALITHA		/	\checkmark		1	
08	10011A0409	MOUNIKA	V			~	\sim	v
69	16811A0470	YERRAMSETTI PRASAD	\bigvee	\checkmark				V
70	15811A0448	MEDISETTY SAI SRI (Re-Admitted)	\checkmark	\checkmark	\checkmark	\bigvee		\checkmark
71	17815A0401	ADARI EEPSITHA	./	./				
72	17815A0402	BONTHU RAMESH BABU		× ×				
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73	17815A0403	CHITIKELA VENKATESH						
74	17815A0404	CHUKKA SRINIVAS	\checkmark	\checkmark	\vee		\checkmark	\checkmark
75	17815A0405	CHUKKALA NANDINI	\checkmark		\langle	\bigvee	\bigvee	\bigvee
76	17815A0406	DALIYA NAGESWARARAO		V	\checkmark	\checkmark		V
77	17815A0407	KADIMISETTI MANIKANTA	./		./			
78	17815A0408	KAMIREDDI SWATHI	V			- V	- /	
79		LOKA DEVI PRASAD	V					V
80	17815A0410	MAJJI NAGA SREENU	V	V	V	V		
81	17815A0411	MALASALA NARENDRA KUMAR	\vee	\vee		\bigvee	V	\bigvee

82	17815A0412	NANDARAPU HARI BHANU PRASAD	/	~	\checkmark	\checkmark	/	
83	17815A0413	NARAPUREDDY JOSHNARAMYA	\checkmark	\checkmark	\checkmark	\checkmark	\checkmark	~
84	17815A0414	NUKARAPU SAIRAM	\checkmark	V	V	~		

R. Anelkemedy

Coordinator

HOD

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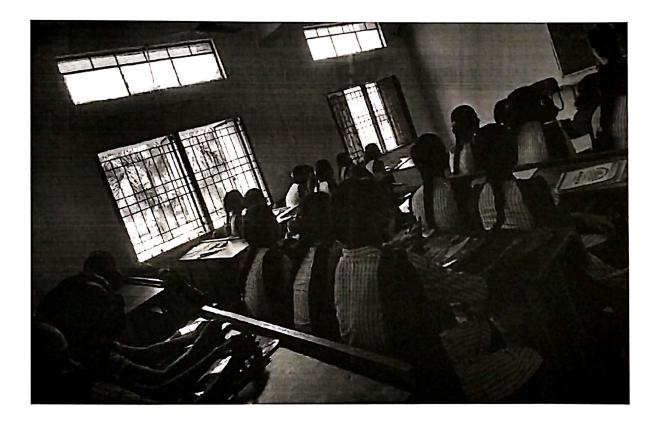
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

A Course on

Embedded System Design

Dated :- 26/07/2018



Coordinator

HOD

HEAD OF THE DEPARTMENT DEPARTMENT OF ECE Avanthi Institute of Engg.&Tech. Makavarapalem, Visakhapatnam Dist-531 113.



AVANTHI

INSTITUTE OF ENGINEERING AND TECHNOLOGY

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Certificate of Participation

participated in the Certificate Course entitled on Embedded System Design conducted from 26-07-2018 to

01-08-2018 was organized by the Department of Electronic and Communication Engineering at Avanthi

Institute of Engineering and Technology.

Coordinator

HOD