JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

COURSE STRUCTURE & SYLLABUS M.Tech ECE Common for

I. Digital Electronics & Communication Engineering (DECE)
II. Digital Electronics & Communication Systems (DECS)
III. Electronics & Communication Engineering (ECE)

Programme

(Applicable for batches admitted from 2019-2020)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

I Semester

S. No.	Course Type/Code	Course Name	Teaching Scheme			Credits
			L	T	P	
1	Core 1	Digital System Design	3	0	0	3
2	Core 2	Digital Data Communications	3	0	0	3
3	Prog. Specific Elective	**Elective I** a. Transform Techniques b.VLSI Technology and Design c. Radar Signal Processing	3	0	0	3
4	Prog. Specific Elective	**Elective II** a. Statistical Signal Processing b. Optical Communication Technology c. Network Security & Cryptography	3	0	0	3
5	Lab 1	System Design Using Verilog HDL Laboratory	0	0	4	2
6	Lab2	Data Communications Laboratory	0	0	4	2
7		Research Methodology and IPR	2	0	0	2
8	Aud 1	Audit Course 1	2	0	0	0
		Total Credits	**16**	**0**	**8**	**18**

II Semester

S. No.	Course Type/Code	Name of the Subject	Teaching Scheme			Credits
			L	T	P	
1	Core 3	Image and Video Processing	3	0	0	3
2	Core 4	Wireless Communications and Networks	3	0	0	3
3	Prog. Specific Elective	**Elective III** a. CMOS Analog & Digital IC Design b. Advanced Computer Architecture c. Soft Computing Techniques	3	0	0	3
4	Prog. Specific Elective	**Elective IV** a. DSP Processors and Architectures b. EMI/ EMC c. Object Oriented Programming	3	0	0	3
5	Lab 1	Advanced Communications Laboratory	0	0	4	2
6	Lab2	Advanced digital Image & video processing Laboratory	0	0	4	2
7		Mini Project	0	0	4	2
8	Aud 2	Audit Course 2	2	0	0	0
		Total Credits	**14**	**0**	**12**	**18**

III Semester

S. No.	Course Type/Code	Subject	Teaching Scheme			Credits
1	Prog. Specific Elective	a) Detection & Estimation Theory b) Advanced Digital Signal Processing c) Coding Theory and Applications	3	0	0	3
2	**Open Elective**	a) Business Analytics b) Industrial Safety c) Operations Research d) Cost Management of Engineering Projects e) Composite Materials f) Waste to Energy	3	0	0	3
3	Dissertation	Dissertation Phase – I	0	0	20	10
	Total		**6**	**0**	**20**	**16**

IV Semester

S. No.	Course Code	Subject	Teaching Scheme			Credits
			L	T	P	
1	Dissertation	Dissertation Phase – II	--	--	32	16
	Total Credits		--	--	32	**16**

Audit course 1 & 2

1. English for Research Paper Writing
2. Disaster Management
3. Sanskrit for Technical Knowledge
4. Value Education
5. Constitution of India
6. Pedagogy Studies
7. Stress Management by Yoga
8. Personality Development through Life Enlightenment Skills.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India

I Year I Semester

L	T	P	C
0	0	4	2

SYSTEM DESIGN USING VERILOG HDL LABORATORY

List of Experiments:

1) Verilog implementation of 8:1 Mux/Demux, Full Adder, 8-bit Magnitude comparator, Encoder/decoder, Priority encoder, D-FF, 4-bit Shift registers (SISO, SIPO, PISO, bidirectional), 3-bit Synchronous Counters, Binary to Gray converter, Parity generator.
2) Sequence generator/detectors, Synchronous FSM – Mealy and Moore machines.
3) Vending machines - Traffic Light controller, ATM, elevator control.
4) PCI Bus & arbiter and downloading on FPGA.
5) UART/ USART implementation in Verilog.
6) Realization of single port SRAM in Verilog.
7) Verilog implementation of Arithmetic circuits like serial adder/ subtractor, parallel adder/subtractor, serial/parallel multiplier.
8) Discrete Fourier transform/Fast Fourier Transform algorithm in Verilog.

Course Outcomes:

At the end of the laboratory work, students will be able to:

1. Identify, formulate, solve and implement problems in signal processing, communication systems etc using RTL design tools.
2. Use EDA tools like Cadence, Mentor Graphics and Xilinx.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India

I Year I Semester

L	T	P	C
0	0	4	2

DATA COMMUNICATIONS LAB

List of Experiments:

1. Study of serial interface RS – 232
2. Study of pc to pc communication using parallel port
3. To establish pc-pc communication using LAN
4. Study of LAN using star topology, bus topology and tree topology
5. Study and configure modem of a computer
6. To configure a hub/switch
7. To study the interconnections of cables for data communication
8. Study of a wireless communication system
9. Set up of time division multiplexing using fiber optics
10. Digital Fiber Optical Transmitter and Receiver

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India

I Year II Semester

L	T	P	C
0	0	4	2

ADVANCED COMMUNICATIONS LAB

Note:

A. Minimum of 10 Experiments have to be conducted
B. All Experiments may be Simulated using MATLAB and to be verified using related training kits.

1. Measurement of Bit Error Rate using Binary Data
2. Verification of minimum distance in Hamming code
3. Determination of output of Convolutional Encoder for a given sequence
4. Determination of output of Convolutional Decoder for a given sequence
5. Efficiency of DS Spread- Spectrum Technique
6. Simulation of Frequency Hopping (FH) system
7. Effect of Sampling and Quantization of Digital Image
8. Verification of Various Transforms (FT / DCT/ Walsh / Hadamard) on a given Image (Finding Transform and Inverse Transform)
9. Point, Line and Edge detection techniques using derivative operators.
10. Implementation of FIR filter using DSP Trainer Kit (C-Code/ Assembly code)
11. Implementation of IIR filter using DSP Trainer Kit (C-Code/ Assembly code)
12. Determination of Losses in Optical Fiber
13. Observing the Waveforms at various test points of a mobile phone using Mobile Phone Trainer
14. Study of Direct Sequence Spread Spectrum Modulation & Demodulation using CDMA-DSS-BER Trainer
15. Study of ISDN Training System with Protocol Analyzer
16. Characteristics of LASER Diode.

Course Outcomes:

At the end of this course, students will be able to

1. Identify the different types of network devices and their functions within anetwork.
2. Understand and build the skills of sub-netting and routingmechanisms.
3. Understand basic protocols of computer networks, and how they can be used to assist in network design andimplementation.

I Year II Semester

L	T	P	C
0	0	4	2

Advanced Digital Image and Video Processing lab

List of Experiments:

1. Perform basic operations on images like addition, subtraction etc.
2. Plot the histogram of an image and perform histogram equalization
3. Implement segmentation algorithms
4. Perform video enhancement
5. Perform video segmentation
6. Perform image compression using lossy technique
7. Perform image compression using lossless technique
8. Perform image restoration
9. Convert a colour model into another
10. Calculate boundary features of an image
11. Calculate regional features of an image
12. Detect an object in an image/video using template matching/Bayes classifier

Course Outcomes:

At the end of this course, students will be able to

1. Perform and analyze image and video enhancement and restoration
2. Perform and analyze image and video segmentation and compression
3. work and process viz., detection, extraction on the image/video

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY: KAKINADA
KAKINADA – 533 003, Andhra Pradesh, India

I Year II Semester

L	T	P	C
0	0	4	2

MINI PROJECT

Syllabus Contents

The students are required to search / gather the material / information on a specific a topic comprehend it and present / discuss in the class.

Course Outcomes

At the end of this course, students will be able to

1. Understand of contemporary / emerging technology for various processes and systems.
2. Share knowledge effectively in oral and written form and formulate documents

III Semester

L	T	P	C
0	0	20	10

(DISSERTATION) DISSERTATION PHASE – I AND PHASE – II

Syllabus Contents:

The dissertation / project topic should be selected / chosen to ensure the satisfaction of the urgent need to establish a direct link between education, national development and productivity and thus reduce the gap between the world of work and the world of study. The dissertation should have the following

- Relevance to social needs of society
- Relevance to value addition to existing facilities in the institute
- Relevance to industry need
- Problems of national importance
- Research and development in various domain

The student should complete the following:

- Literature survey Problem Definition
- Motivation for study and Objectives
- Preliminary design / feasibility / modular approaches
- Implementation and Verification
- Report and presentation

The dissertation stage II is based on a report prepared by the students on dissertation allotted to them. It may be based on:

- Experimental verification / Proof of concept.
- Design, fabrication, testing of Communication System.
- The viva-voce examination will be based on the above report and work.

Guidelines for Dissertation Phase – I and II at M. Tech. (Electronics):

- As per the AICTE directives, the dissertation is a yearlong activity, to be carried out and evaluated in two phases i.e. Phase – I: July to December and Phase – II: January to June.
- The dissertation may be carried out preferably in-house i.e. department's laboratories and centers OR in industry allotted through department's T & P coordinator.
- After multiple interactions with guide and based on comprehensive literature survey, the student shall identify the domain and define dissertation objectives. The referred literature should preferably include IEEE/IET/IETE/Springer/Science Direct/ACM journals in the areas of Computing and Processing (Hardware and Software), Circuits-Devices and Systems, Communication-Networking and Security, Robotics and Control Systems, Signal Processing and Analysis and any other related domain. In case of Industry sponsored projects, the relevant application notes, while papers, product catalogues should be referred and reported.
- Student is expected to detail out specifications, methodology, resources required, critical issues involved in design and implementation and phase wise work distribution, and submit the proposal within a month from the date of registration.
- Phase – I deliverables: A document report comprising of summary of literature survey, detailed objectives, project specifications, paper and/or computer aided design, proof of concept/functionality, part results, A record of continuous progress.
- Phase – I evaluation: A committee comprising of guides of respective specialization shall assess the progress/performance of the student based on report, presentation and Q &A. In case of unsatisfactory performance, committee may recommend repeating the Phase-I work.

IV Semester

L	T	P	C
0	0	32	16

(DISSERTATION) DISSERTATION PHASE – I AND PHASE – II

Syllabus Contents:

The dissertation / project topic should be selected / chosen to ensure the satisfaction of the urgent need to establish a direct link between education, national development and productivity and thus reduce the gap between the world of work and the world of study. The dissertation should have the following

- Relevance to social needs of society
- Relevance to value addition to existing facilities in the institute
- Relevance to industry need
- Problems of national importance
- Research and development in various domain

The student should complete the following:

- Literature survey Problem Definition
- Motivation for study and Objectives
- Preliminary design / feasibility / modular approaches
- Implementation and Verification
- Report and presentation

The dissertation stage II is based on a report prepared by the students on dissertation allotted to them. It may be based on:

- Experimental verification / Proof of concept.
- Design, fabrication, testing of Communication System.
- The viva-voce examination will be based on the above report and work.

Guidelines for Dissertation Phase – I and II at M. Tech. (Electronics):

- As per the AICTE directives, the dissertation is a yearlong activity, to be carried out and evaluated in two phases i.e. Phase – I: July to December and Phase – II: January to June.
- The dissertation may be carried out preferably in-house i.e. department's laboratories and centers OR in industry allotted through department's T & P coordinator.
- After multiple interactions with guide and based on comprehensive literature survey, the student shall identify the domain and define dissertation objectives. The referred literature should preferably include IEEE/IET/IETE/Springer/Science Direct/ACM journals in the areas of Computing and Processing (Hardware and Software), Circuits-Devices and Systems, Communication-Networking and Security, Robotics and Control Systems, Signal Processing and Analysis and any other related domain. In case of Industry sponsored projects, the relevant application notes, while papers, product catalogues should be referred and reported.
- Student is expected to detail out specifications, methodology, resources required, critical issues involved in design and implementation and phase wise work distribution, and submit the proposal within a month from the date of registration.
- Phase – I deliverables: A document report comprising of summary of literature survey, detailed objectives, project specifications, paper and/or computer aided design, proof of concept/functionality, part results, A record of continuous progress.
- Phase – I evaluation: A committee comprising of guides of respective specialization shall assess the progress/performance of the student based on report, presentation and Q &A. In case of unsatisfactory performance, committee may recommend repeating the Phase-I work.

- During phase – II, student is expected to exert on design, development and testing of the proposed work as per the schedule. Accomplished results/contributions/innovations should be published in terms of research papers in reputed journals and reviewed focused conferences OR IP/Patents.
- Phase – II deliverables: A dissertation report as per the specified format, developed system in the form of hardware and/or software, a record of continuous progress.
- Phase – II evaluation: Guide along with appointed external examiner shall assess the progress/performance of the student based on report, presentation and Q &A. In case of unsatisfactory performance, committee may recommend for extension or repeating the work

Course Outcomes:

At the end of this course, students will be able to

5. Ability to synthesize knowledge and skills previously gained and applied to an in-depth study and execution of new technical problem.
6. Capable to select from different methodologies, methods and forms of analysis to produce a suitable research design, and justify their design.
7. Ability to present the findings of their technical solution in a written report.
8. Presenting the work in International/ National conference or reputed journals.