COMPARATIVE STUDY OF HIGHER ORDER REDUCTION METHODS

A project report submitted in partial fulfillment of the requirements
For the award of the degree of

BACHELOR OF TECHNOLOGY
IN
ELECTRICAL & ELECTRONICS ENGINEERING

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CERTIFICATE

This is certify that the project report entitled **"COMPARATIVE STUDY OF HIGHER ORDER REDUCTION METHODS"** is a bonafide work submitted by **CH KRANTHI MOHAN, P DIVYA PRACHOTHAN, S APPALA RAJU, S HAREESH** and **J VENKATA NAVEEN KUMAR** in partial fulfillment of the requirements for the award of degree of

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ABSTRACT

In this project a method is proposed for finding stable reduced order models of single-input-single-output large scale systems using Factor division algorithm and the mixed mathematical method. The denominator polynomial of the reduced order model with respect to original model is determined by forming the clusters of the numerator polynomial with respect to original model are obtained by using the mixed mathematical method. The mixed methods are simple and guarantee the stability of the reduced model if the original system is stable. The methodology of the proposed methods illustrated with the help of examples from literature.