

PRESENT LIGHT WEIGHT CIPHER

A project report submitted in partial fulfillment of the requirements

For the award of the

BACHELOR OF TECHNOLOGY

IN

“ELECTRONICS & COMMUNICATION ENGINEERING”

Submitted by

M. CHANDU	15811A0441
G. TEJAVATHI	15811A0427
M. GIRIDHAR	15811A0449
G. HARSHAK SRAVANTH	15811A0428

Under the esteemed guidance of

Mr.T.PATTALU NAIDU, M.Tech.,(Ph.D)

Assistant Professor



DEPARTMENT OF

ELECTRONICS AND COMMUNICATION ENGINEERING

AVANTHI INSTITUTE OF ENGINEERING & TECHNOLOGY

(Approved by AICTE and Permanently Affiliated to JNTU- KAKINADA, AP)

(An NBA,NAAC Accredited Institution)

TAMARAM (V), MAKAVARAPALEM (M), VISAKHAPATNAM – 533113

2015 – 2019

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TAMARAM (V), MAKAVARAPALEM (M), VISAKHAPATNAM DISTRICT-531113

**DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING**



CERTIFICATE

This is certify that the project report entitled “PRESENT LIGHT WEIGHT CIPHER” is a bonafide work submitted by **Madhumanthi Chandu, Gorrepati Tejavathi, Mekala Giridhar, Gundubogula Harshak Sravanth**, in partial fulfillment of the requirements for the award of

BACHELOR OF TECHNOLOGY

IN

ELECTRONICS & COMMUNICATION ENGINEERING

During the academic year **2015-2019**.

T.P.Naidu

INTERNAL GUIDE

Mr. T.PATTALU NAIDU, M.Tech., (Ph.D)

Assistant professor

ECE, AIET

E. Govinda
HEAD OF DEPARTMENT

Mr. E. GOVINDA, M.Tech., (Ph.D)

Associate professor

ECE,AIET

Srinivasulu

EXTERNAL EXAMINER

ABSTRACT

Security and privacy are of prime concern in the emerging internet of things (IOT) and cyber physical systems(CPS) based applications. Light weight cryptography plays an essential role in securing the data in this emerging pervasive computing environments. In this paper, we propose a high performance and area efficient VLSI architecture with 64bit data path for the PRESENT block cipher. The architecture is synthesized for the available on the Xilinx ML-505 platform.